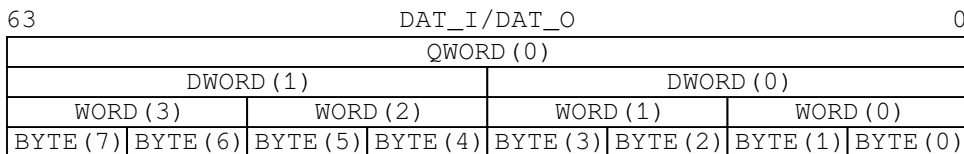
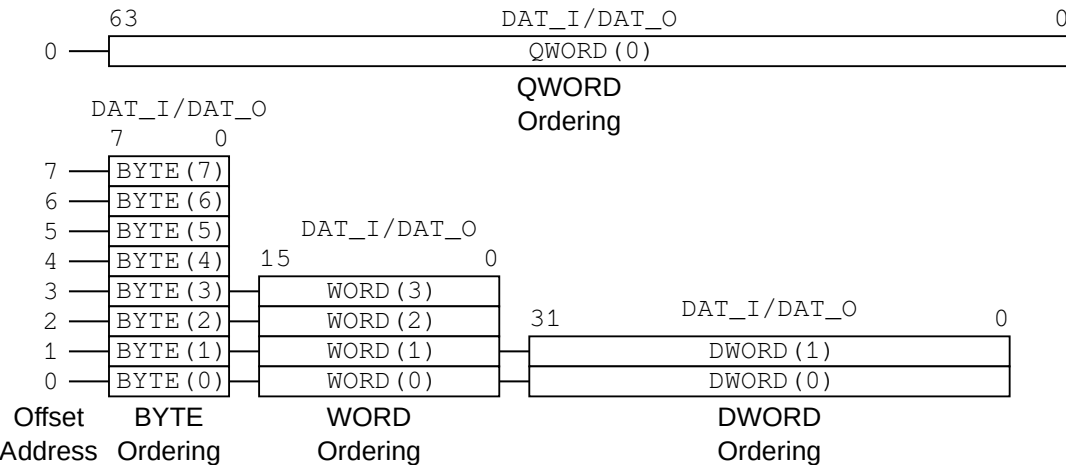


a) Big Endian BYTE, WORD, DWORD and QWORD positioning in a 64-bit operand.



b) Little Endian BYTE, WORD, DWORD and QWORD positioning in a 64-bit operand.



c) Address nomenclature.